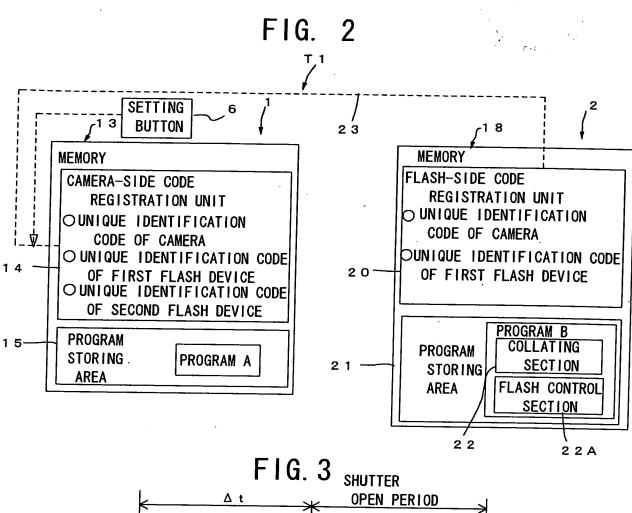
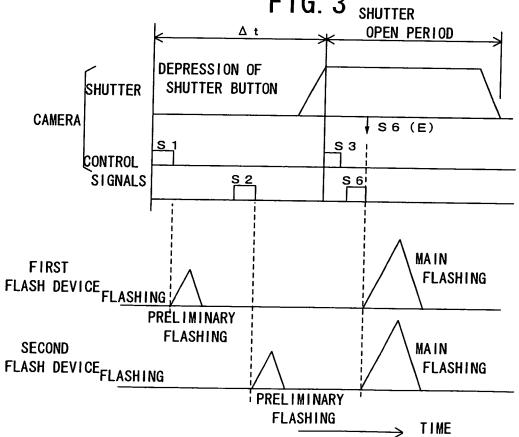
FIG. 1 23 1, 3 RADIO MEMORY FLASH RECEIVING 19 FLASH-SIDE DETERMINING PARŢ CODE CIRCUIT √ 2 O CPU REGISTRATION MEMORY CPU UNIT CAMERA-SIDE **PROGRAM** CODE ìο STORING REGISTRATION **AREA** UNIT 1 2 FLASH BODY UNIT **PROGRAM** RADIO STORING TRANSMITTING AREA **PART** 1 6 1 5 23 1 1 8 REFLECTION RADIO **OBJECT** MEMORY RECEIVING
PART
V 2 0 19 FLASH-SIDE H-CODE REGISTRATION CPU UNIT **PROGRAM** STORING AREA FLASH BODY UNIT 1 6





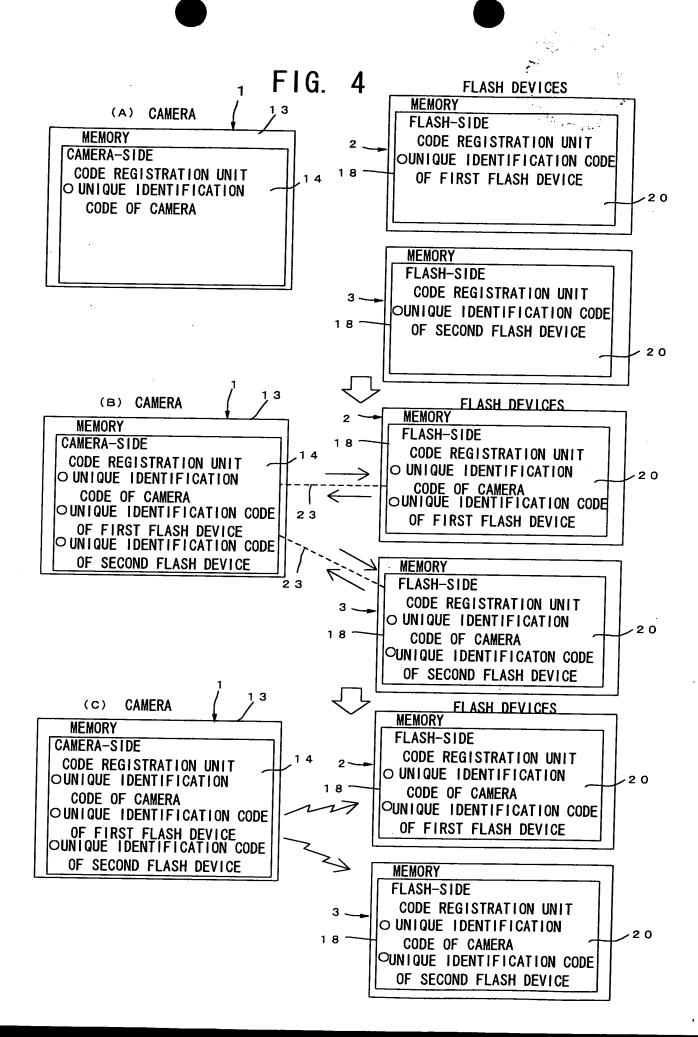


FIG. 5

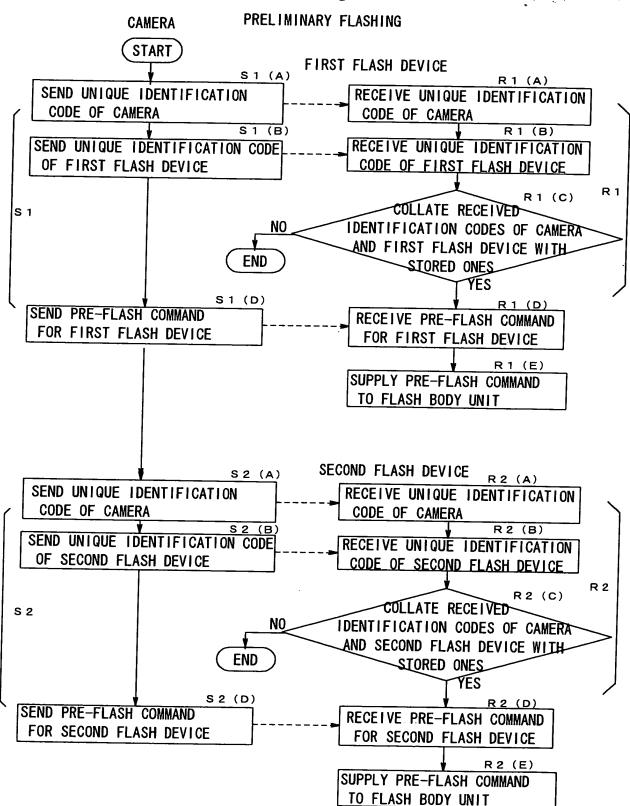
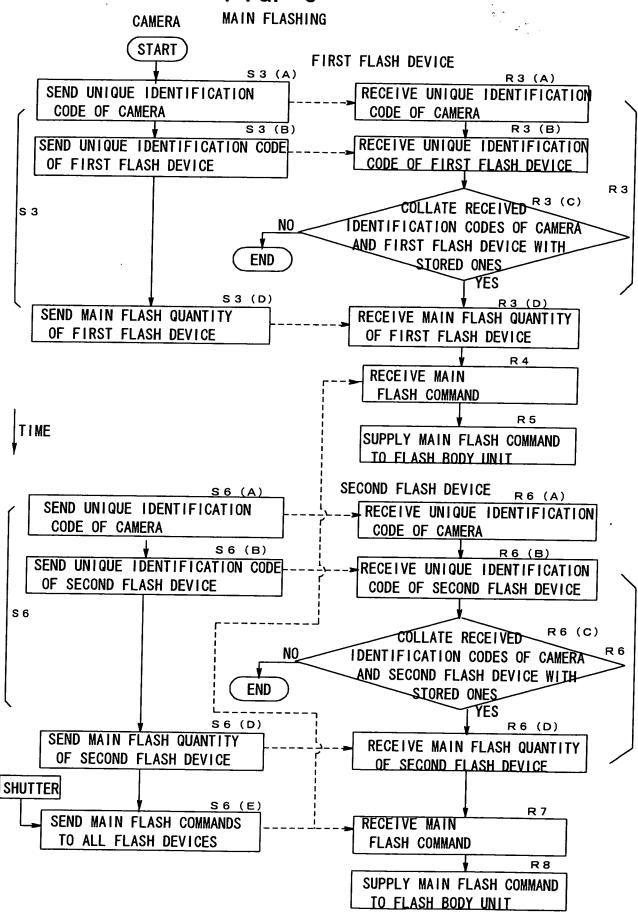
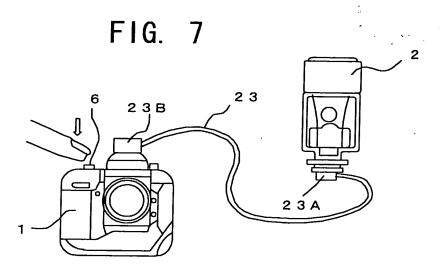
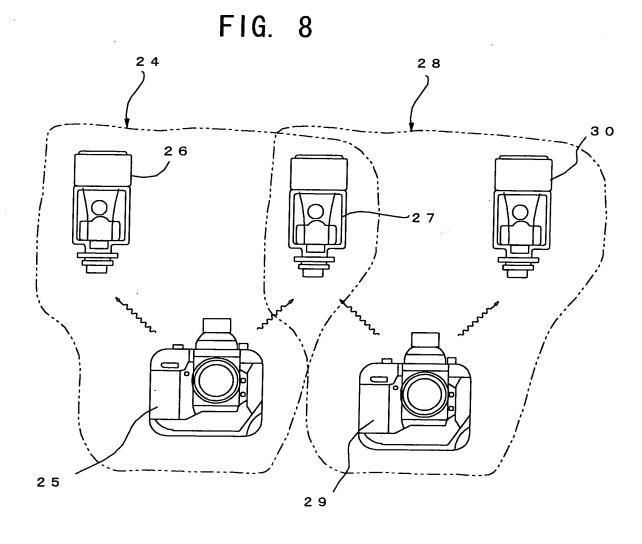
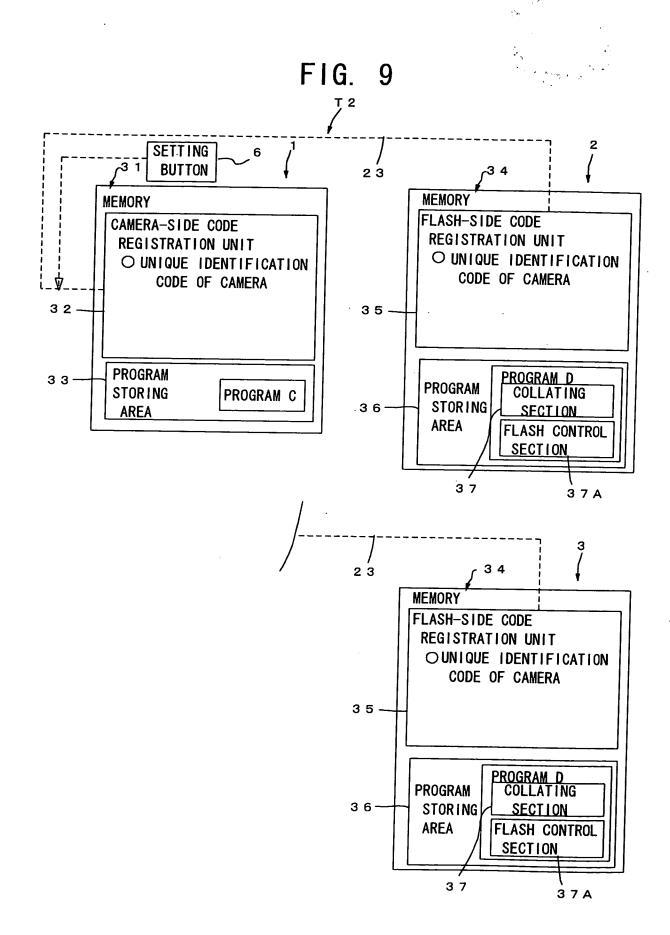


FIG. 6









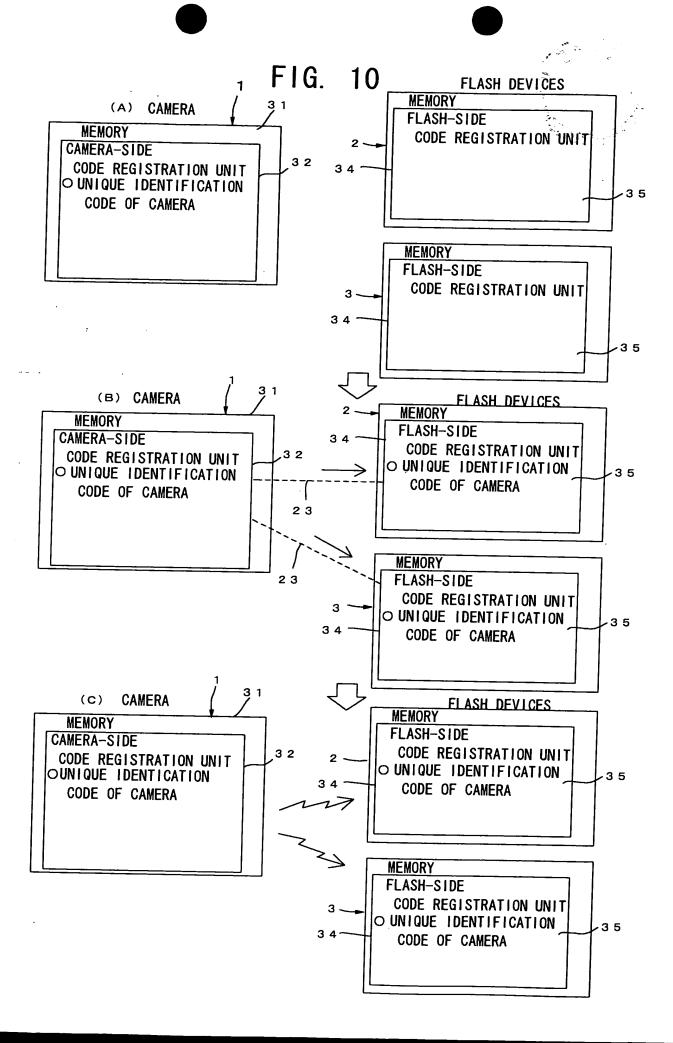


FIG. 11

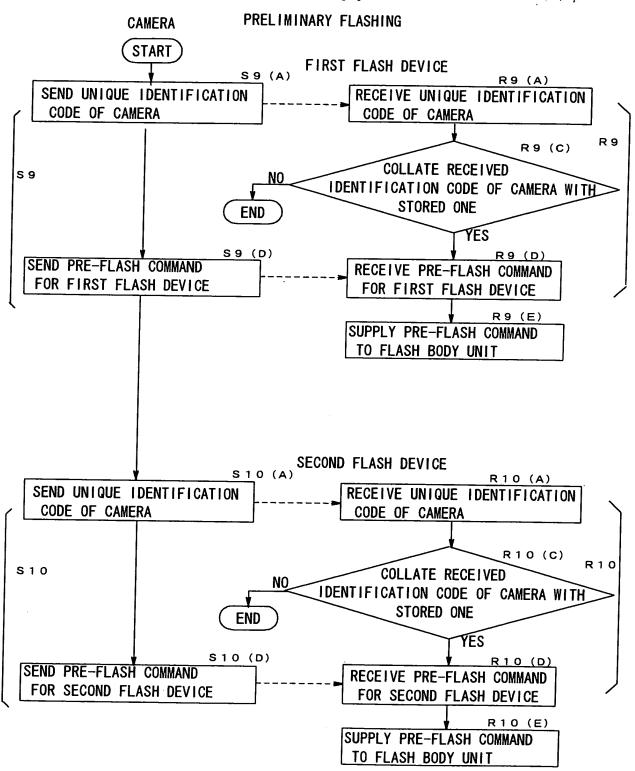


FIG. 12

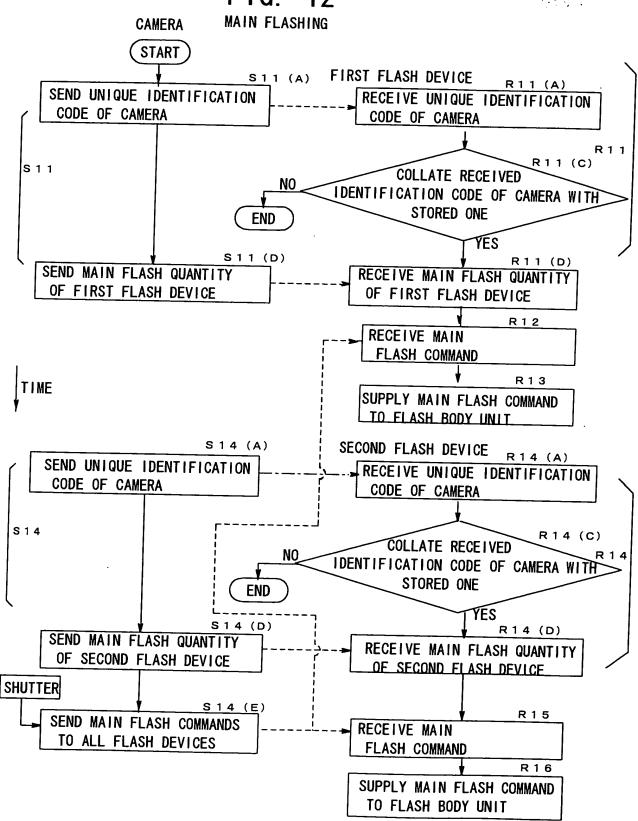
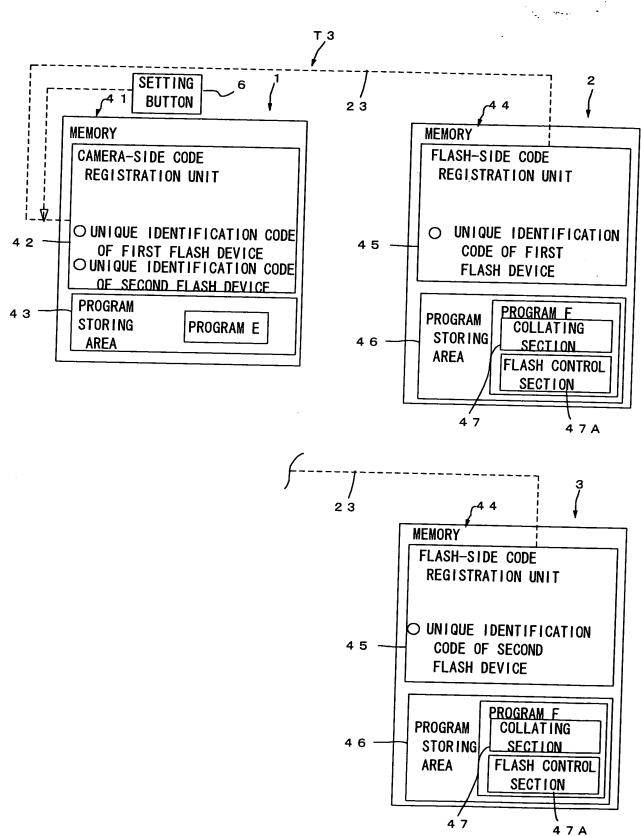


FIG. 13



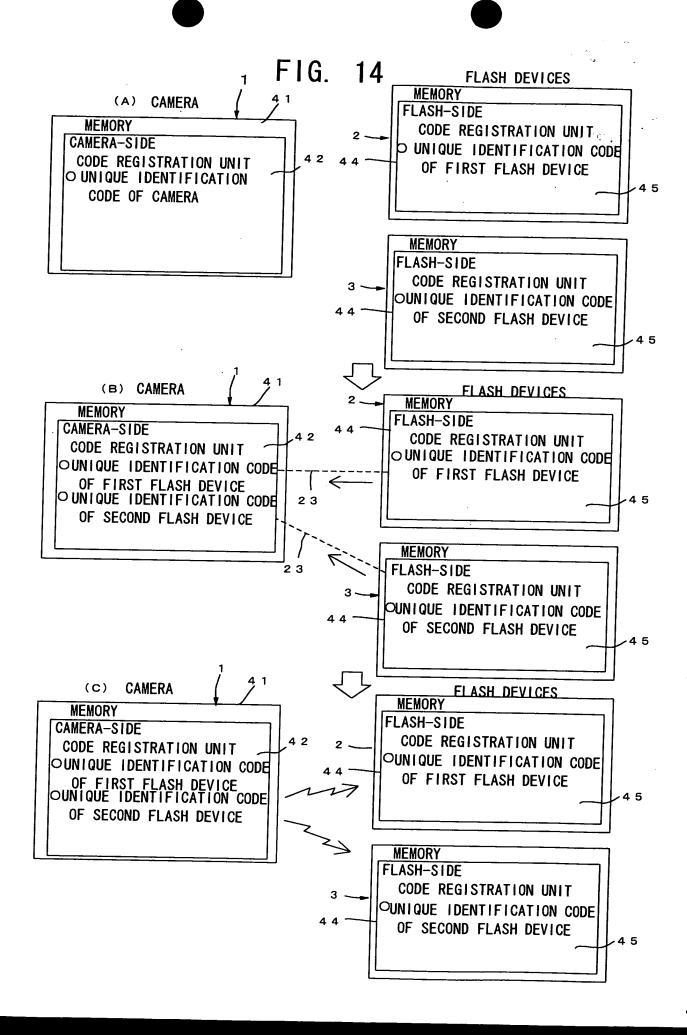


FIG. 15
PRELIMINARY FLASHING

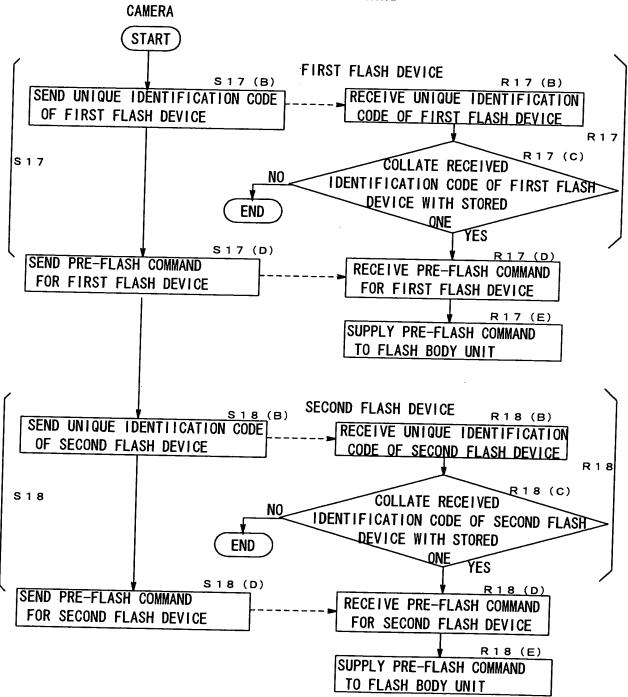


FIG. 16

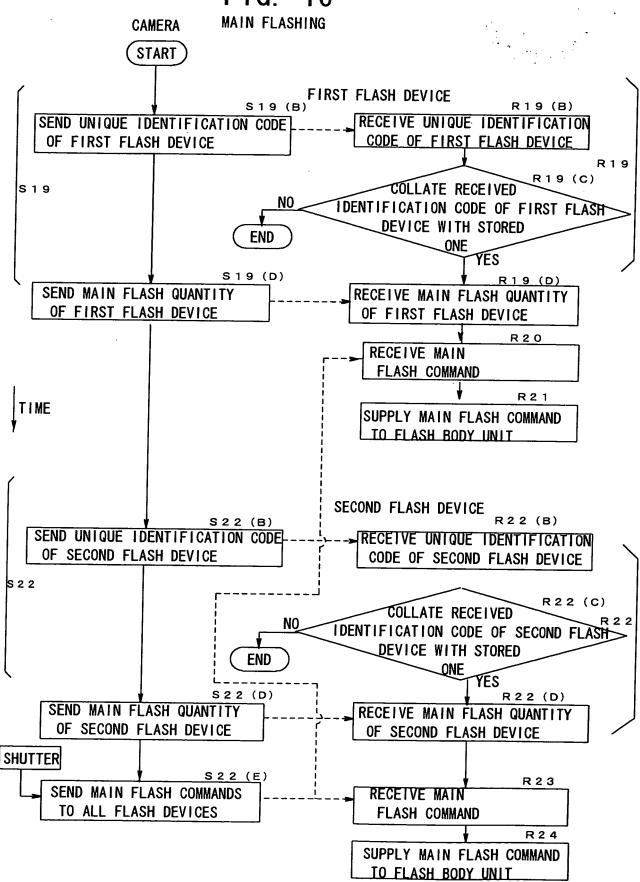


FIG. 17

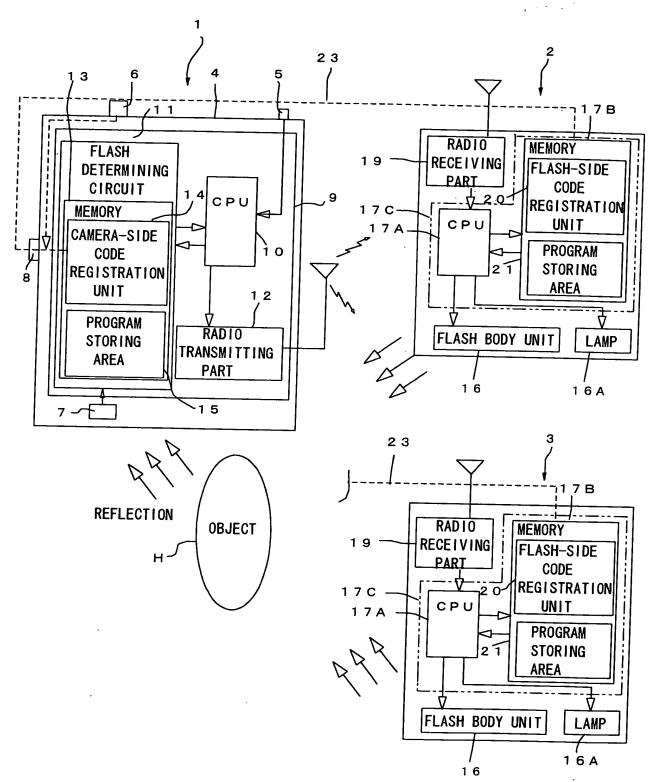


FIG. 18

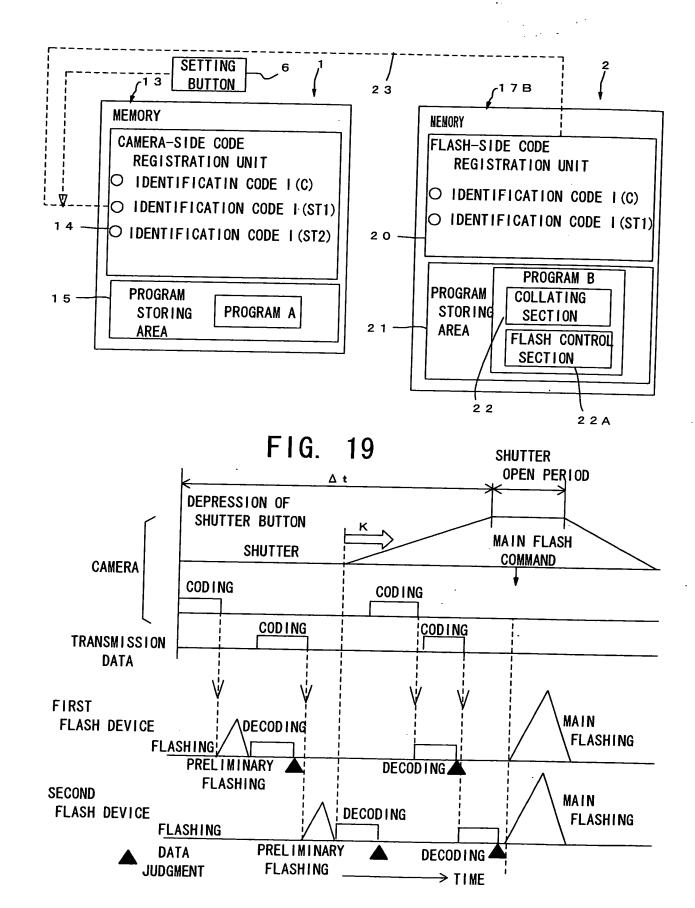
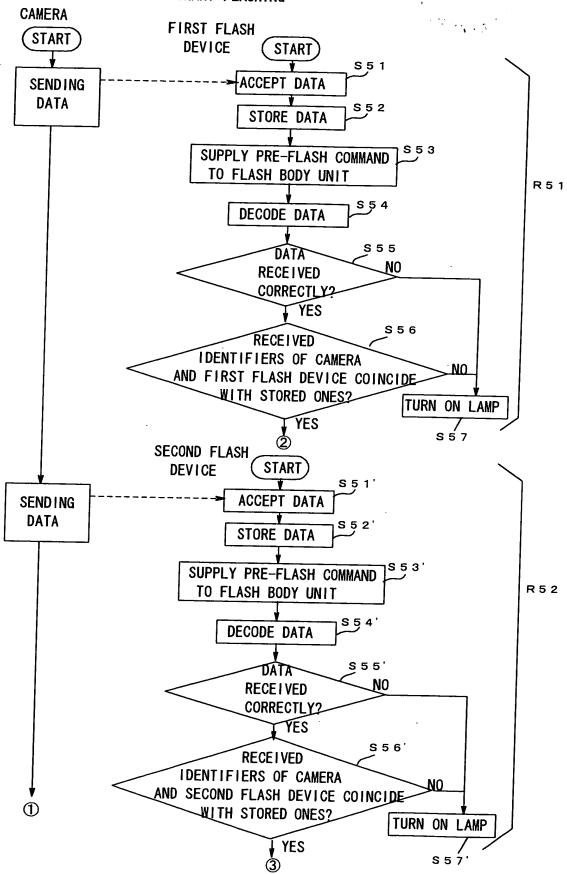


FIG. 20 PRELIMINARY FLASHING



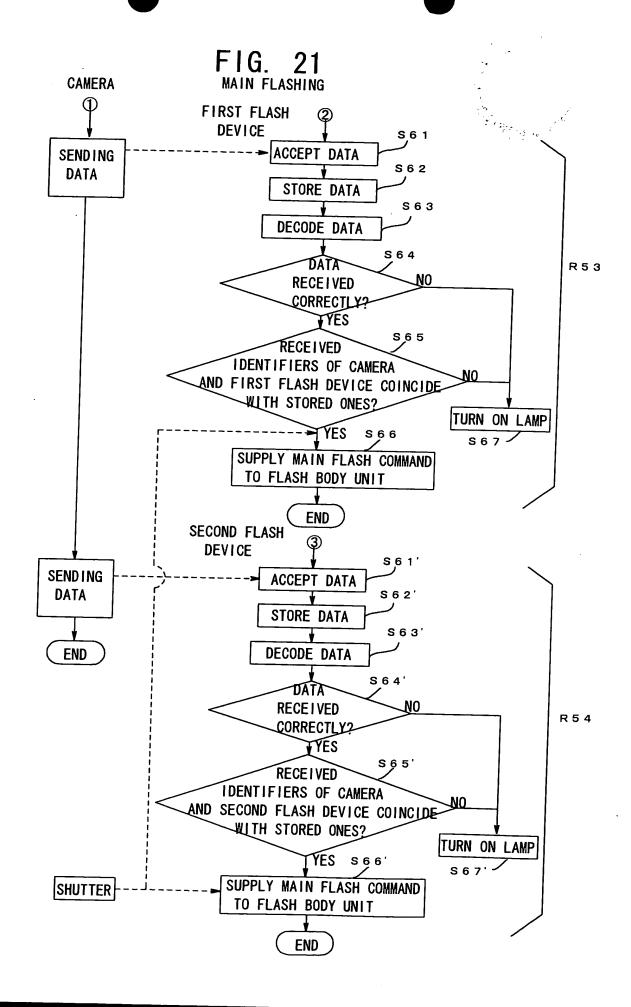
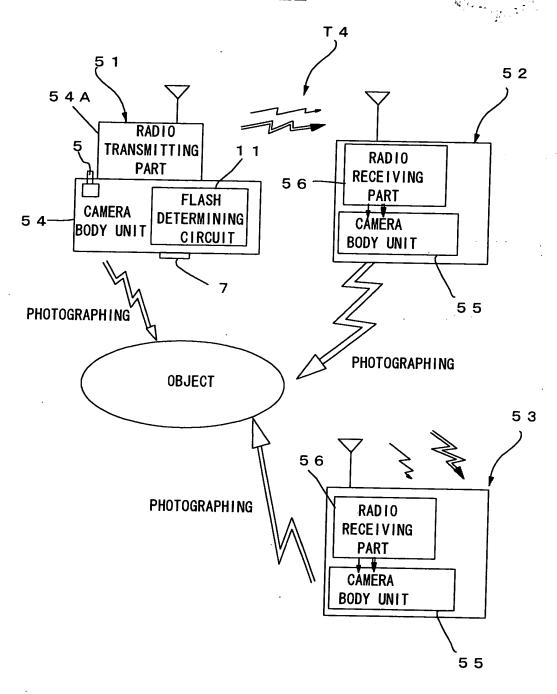


FIG. 22



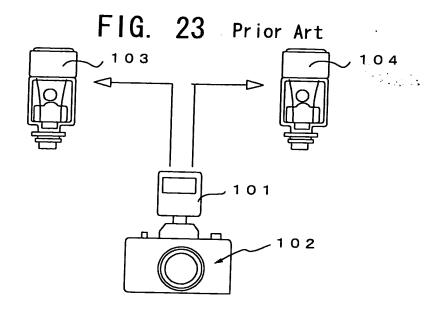


FIG. 24 Prior Art PRELIMINARY FLASHING

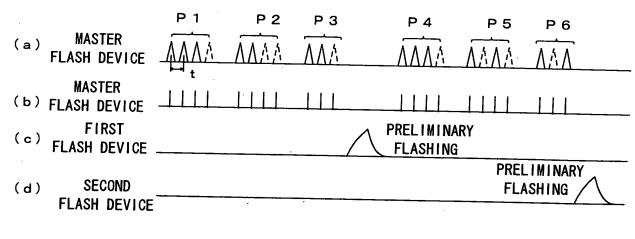


FIG. 25 Prior Art

